Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A method for validating a bus, comprising:

taking a snapshot of configuration registers of selected bus devices coupled to a host bus adapter;

storing values from the snapshot of configuration registers of selected bus devices;

power cycling the host bus adapter; and

re-initializing the configuration registers of the selected bus devices, wherein the values are stored in a file dedicated to configuration information storage, the file dedicated to configuration information storage being an .ini file.

2. (Original) The method of Claim 1, wherein the re-initializing of the configuration registers of the selected bus devices is performed in a recursive manner.

3-5. (Canceled)

6. (Currently Amended) The method of Claim 1 [[3]], further comprising creating at least one data pattern in a memory of the host bus adapter before power cycling the host bus adapter.

7. (Original) The method of Claim 6, further comprising powering down the host bus adapter for a predefined period and, after the predefined period expires, powering up the host bus adapter.

8. (Original) The method of Claim 7, loading the configuration registers of the selected bus devices with the stored values of the snapshot.

9. (Original) The method of Claim 8, further comprising verifying the at least one data pattern in memory of the host bus adapter.

10. (Original) The method of Claim 9, further comprising the host bus adapter as one of the group consisting of pass and fail.

11. (Original) The method of Claim 1, wherein the host bus adapter is a Peripheral Component Interconnect (PCI) host bus adapter.

12. (Currently Amended) A system for validating a host bus adapter, comprising:

a host bus;

a processor, the processor including an operating system;

a main memory coupled to the ~~host~~ processor through the host bus;

a first bus; and

a host bus adapter coupled to the processor through the host bus, wherein the processor takes a snapshot of configuration registers of selected devices through the first bus before conducting a test of the host bus adapter, the host bus adapter being powered down for a period of tens seconds and then is powered up before testing the host bus adapter, the snapshot of configuration registers being stored in a file maintained by the operating system.

13. (Original) The system of Claim 12, wherein the first bus is a Peripheral Component Interconnect (PCI) bus.

14-20. (Canceled)

21. (Original) A method for validating a Peripheral Component Interconnect (PCI) host bus adapter, comprising:

reading values of all configuration registers of select PCI devices;

storing the values in an .ini file;

creating a data pattern in memory of a Redundant Array of Inexpensive Disks (RAID) controller adapter using a command mailbox protocol;

switching off power to a raiser card using a general purpose input/output (IO) port;

waiting a predefined period of time;

switching on power to the raiser card using the general purpose IO port;

loading all the configuration registers of the select PCI devices with the values from the .ini file;

initializing all configuration registers of the select PCI devices from the loaded values; and

verifying the data pattern in the memory of the RAID controller adapter using the command mailbox protocol.

22. (Original) The method of Claim 21, further comprising stamping the host bus adapter as one of the group consisting of PASS and FAIL depending on results from verifying the data pattern.

23. (Original) The method of Claim 22, wherein the raiser card is an ADEX raiser card.